

Group Art Unit: 2813

Examiner: Not Yet Assigned

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Husam N. Al-Shareef, et al.

Application No.: 09/912,558

rppiication 110.. 07/712,000

Filed: July 26, 2001

For: FABRICATION OF DRAM AND OTHER

SEMICONDUCTOR DEVICES WITH AN

INSULATING FILM USING A WET

RAPID THERMAL OXIDATION PROCESS

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents Washington, DC 20231

Dear Sir:

Submitted herewith is one set (eight sheets, eight figures) of formal drawings for filing in the above-identified Patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: November 8, 2001

Respectfully submitted,

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APPROVED		
BY	CLASS	SUBCLASS
DRAFTSMAN		

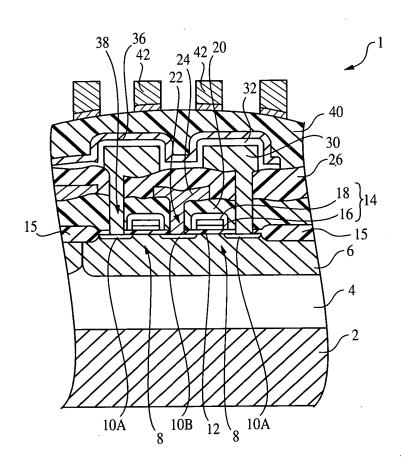


FIG. 1

D991255.110801

FIG. 2

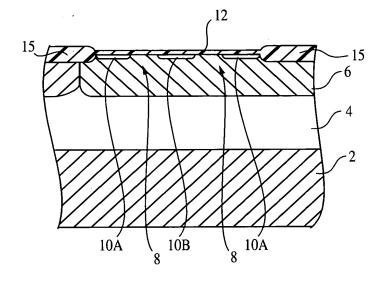


FIG. 3

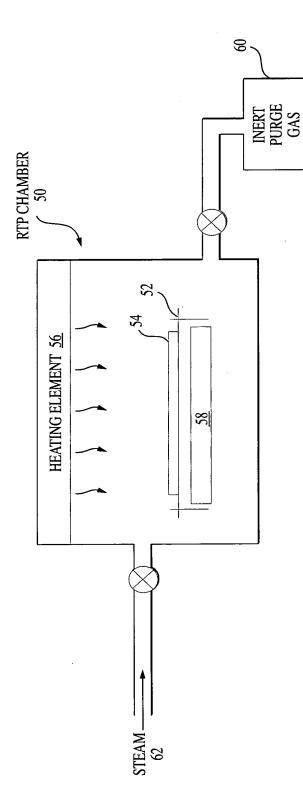


FIG. 4

COSISS ILUSCI

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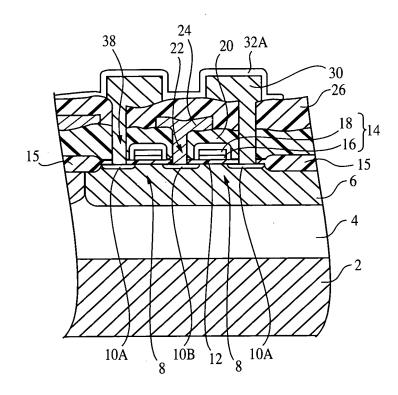


FIG. 5

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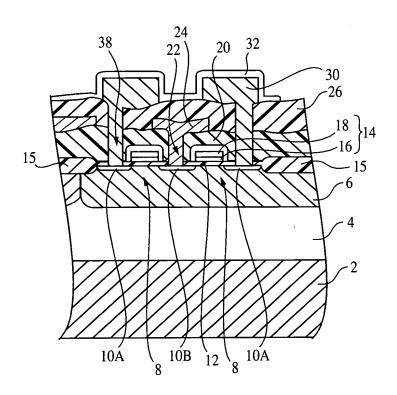


FIG. 6





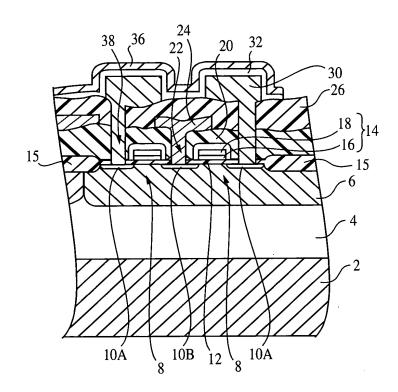


FIG. 7

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APPROVED	O.G. FIG.		
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DEPOSIT DIELECTRIC OR INSULATING FILM

SUBJECT THE INSULATING FILM TO A HEAT TREATMENT IN AN AMBIENT COMPRISING A STABILIZING GAS INCLUDING A GAS SELECTED FROM THE GROUP CONSISTING OF N2, 02, 03, NO, OR N2O

SUBJECT THE INSULATING FILM TO A WET OXIDATION IN A RAPID THERMAL PROCESS (RTP) CHAMBER

FIG. 8